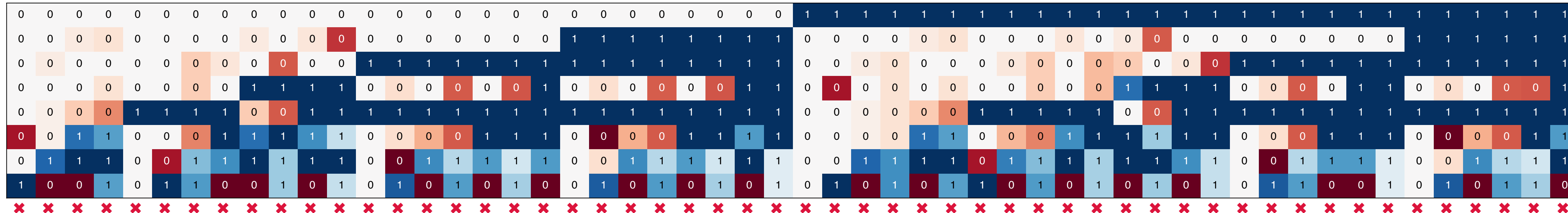


M1 M2 M3

M2 M3

M3

Input Monomer



X X X

Logic Gates

Ern

